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12/11/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10014680	FILING DATE 12/11/2001	CLASS 327	SUBCLASS 175	GAU 286 2801	EXAMINER LHU
*APPLICANTS: Chawane Vijay;					
**CONTINUING DATA VERIFIED:					
FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claim: <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO.			
35 USC 119 conditions met: <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		5298-07400 CD01191			
Verified and Acknowledged Examiners's initials					
TITLE: Circuit and method for adjusting duty cycle of a high speed, low swing output signal					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		AN T LHU Assistant Examiner	CLAIMS ALLOWED	
			Total Claims 13	Print Claim for 0.0 1
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drawn 3	Figs. Drawn 8
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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